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KEY=SYSTEM - GLORIA KENYON

RTL HARDWARE DESIGN USING VHDL

CODING FOR EFFICIENCY, PORTABILITY, AND SCALABILITY

[John Wiley & Sons](#) **The skills and guidance needed to master RTL hardware design This book teaches readers how to systematically design efficient, portable, and scalable Register Transfer Level (RTL) digital circuits using the VHDL hardware description language and synthesis software. Focusing on the module-level design, which is composed of functional units, routing circuit, and storage, the book illustrates the relationship between the VHDL constructs and the underlying hardware components, and shows how to develop codes that faithfully reflect the module-level design and can be synthesized into efficient gate-level implementation. Several unique features distinguish the book: * Coding style that shows a clear relationship between VHDL constructs and hardware components * Conceptual diagrams that illustrate the realization of VHDL codes * Emphasis on the code reuse * Practical examples that demonstrate and reinforce design concepts, procedures, and techniques * Two chapters on realizing sequential algorithms in hardware * Two chapters on scalable and parameterized designs and coding * One chapter covering the synchronization and interface between multiple clock domains Although the focus of the book is RTL synthesis, it also examines the synthesis task from the perspective of the overall development process. Readers learn good design practices and guidelines to ensure that an RTL design can accommodate future simulation, verification, and testing needs, and can be easily incorporated into a larger system or reused. Discussion is independent of technology and can be applied to both ASIC and FPGA devices. With a balanced presentation of fundamentals and practical examples, this is an excellent textbook for upper-level undergraduate or graduate courses in advanced digital logic. Engineers who need to make effective use of today's synthesis software and FPGA devices should also refer to this book.**

EFFECTIVE CODING WITH VHDL

PRINCIPLES AND BEST PRACTICE

[MIT Press](#) **A guide to applying software design principles and coding practices to VHDL to improve the readability, maintainability, and quality of VHDL code. This book addresses an often-neglected aspect of the creation of VHDL designs. A VHDL description is also source code, and VHDL designers can use the best practices of software development to write high-quality code and to organize it in a design. This book presents this unique set of skills, teaching VHDL designers of all experience levels how to apply the best design principles and coding practices from the software world to the world of hardware. The concepts introduced here will help readers write code that is easier to understand and more likely to be correct, with improved readability, maintainability, and overall quality. After a brief review of VHDL, the book presents fundamental design principles for writing code, discussing such topics as design, quality, architecture, modularity, abstraction, and hierarchy. Building on these concepts, the book then introduces and provides recommendations for each basic element of VHDL code, including statements, design units, types, data objects, and subprograms. The book covers naming data objects and functions, commenting the source code, and visually presenting the code on the screen. All recommendations are supported by detailed rationales. Finally, the book explores two uses of VHDL: synthesis and testbenches. It examines the key characteristics of code intended for synthesis (distinguishing it from code meant for simulation) and then demonstrates the design and implementation of testbenches with a series of examples that verify different kinds of models, including combinational, sequential, and FSM code. Examples from the book are also available on a companion website, enabling the reader to experiment with the complete source code.**

SOFTWARE-DEFINED RADIO FOR ENGINEERS

[Artech House](#) **Based on the popular Artech House classic, Digital Communication Systems Engineering with Software-Defined Radio, this book provides a practical approach to quickly learning the software-defined radio (SDR) concepts needed for work in the field. This up-to-date volume guides readers on how to quickly prototype wireless designs using SDR for real-world testing and experimentation. This book explores advanced wireless communication techniques such as OFDM, LTE, WLA, and hardware targeting. Readers will gain an understanding of the core concepts behind wireless hardware, such as the radio frequency front-end, analog-to-digital and digital-to-analog converters, as well as various processing technologies. Moreover, this volume includes chapters on timing estimation, matched filtering, frame**

synchronization message decoding, and source coding. The orthogonal frequency division multiplexing is explained and details about HDL code generation and deployment are provided. The book concludes with coverage of the WLAN toolbox with OFDM beacon reception and the LTE toolbox with downlink reception. Multiple case studies are provided throughout the book. Both MATLAB and Simulink source code are included to assist readers with their projects in the field.

PROCEEDINGS, ... INTERNATIONAL SYMPOSIUM ON VLSI DESIGN

EMBEDDED SYSTEMS DESIGN WITH FPGAS

[Springer Science & Business Media](#) This book presents the methodologies and for embedded systems design, using field programmable gate array (FPGA) devices, for the most modern applications. Coverage includes state-of-the-art research from academia and industry on a wide range of topics, including applications, advanced electronic design automation (EDA), novel system architectures, embedded processors, arithmetic, and dynamic reconfiguration.

DIGITAL INTEGRATED CIRCUIT DESIGN

FROM VLSI ARCHITECTURES TO CMOS FABRICATION

[Cambridge University Press](#) This practical, tool-independent guide to designing digital circuits takes a unique, top-down approach, reflecting the nature of the design process in industry. Starting with architecture design, the book comprehensively explains the why and how of digital circuit design, using the physics designers need to know, and no more.

FPGA PROTOTYPING BY VHDL EXAMPLES

XILINX SPARTAN-3 VERSION

[John Wiley & Sons](#) This book uses a "learn by doing" approach to introduce the concepts and techniques of VHDL and FPGA to designers through a series of hands-on experiments. FPGA Prototyping by VHDL Examples provides a collection of clear, easy-to-follow templates for quick code development; a large number of practical examples to illustrate and reinforce the concepts and design techniques; realistic projects that can be implemented and tested on a Xilinx prototyping board; and a thorough exploration of the Xilinx PicoBlaze soft-core microcontroller.

THE STUDENT'S GUIDE TO VHDL

[Elsevier](#) The Student's Guide to VHDL is a condensed edition of The Designer's Guide to VHDL, the most widely used textbook on VHDL for digital system modeling. The Student's Guide is targeted as a supplemental reference book for computer organization and digital design courses. Since publication of the first edition of The Student's Guide, the IEEE VHDL and related standards have been revised. The Designer's Guide has been revised to reflect the changes, so it is appropriate that The Student's Guide also be revised. In The Student's Guide to VHDL, 2nd Edition, we have included a design case study illustrating an FPGA-based design flow. The aim is to show how VHDL modeling fits into a design flow, starting from high-level design and proceeding through detailed design and verification, synthesis, FPGA place and route, and final timing verification. Inclusion of the case study helps to better serve the educational market. Currently, most college courses do not formally address the details of design flow. Students may be given informal guidance on how to proceed with lab projects. In many cases, it is left to students to work it out for themselves. The case study in The Student's Guide provides a reference design flow that can be adapted to a variety of lab projects.

FPGA IMPLEMENTATIONS OF NEURAL NETWORKS

[Springer Science & Business Media](#) During the 1980s and early 1990s there was significant work in the design and implementation of hardware neurocomputers. Nevertheless, most of these efforts may be judged to have been unsuccessful: at no time have hardware neurocomputers been in wide use. This lack of success may be largely attributed to the fact that earlier work was almost entirely aimed at developing custom neurocomputers, based on ASIC technology, but for such niche - eas this technology was never sufficiently developed or competitive enough to justify large-scale adoption. On the other hand, gate-arrays of the period mentioned were never large enough nor fast enough for serious artificial-neural-network (ANN) applications. But technology has now improved: the capacity and performance of current FPGAs are such that they present a much more realistic alternative. Consequently neurocomputers based on FPGAs are now a much more practical proposition than they have been in the past. This book summarizes some work towards this goal and consists of 12 papers that were selected, after review, from a number of submissions. The book is nominally divided into three parts: Chapters 1 through 4 deal with foundational issues; Chapters 5 through 11 deal with a variety of implementations; and Chapter 12 looks at the lessons learned from a large-scale project and also reconsiders design issues in light of current and future technology.

DIGITAL SYSTEM DESIGN WITH VHDL

[Pearson Education](#) Digital System Design with VHDL is intended both for students on Digital Design courses and practitioners who would like to integrate digital design and VHDL synthesis in the workplace. Its unique approach combines the principles of digital design with a guide to the use of VHDL. Synthesis issues are discussed and practical guidelines are provided for improving simulation accuracy and performance.

A PRACTICAL INTRODUCTION TO HARDWARE/SOFTWARE CODESIGN

[Springer Science & Business Media](#) This is a practical book for computer engineers who want to understand or implement hardware/software systems. It focuses on problems that require one to combine hardware design with software design - such problems can be solved with hardware/software codesign. When used properly, hardware/software codesign works better than hardware design or software design alone: it can improve the overall performance of digital systems, and it can shorten their design time. Hardware/software codesign can help a designer to make trade-offs between the flexibility and the performance of a digital system. To achieve this, a designer needs to combine two radically different ways of design: the sequential way of decomposition in time, using software, with the parallel way of decomposition in space, using hardware. **Intended Audience** This book assumes that you have a basic understanding of hardware that you are familiar with standard digital hardware components such as registers, logic gates, and components such as multiplexers and arithmetic operators. The book also assumes that you know how to write a program in C. These topics are usually covered in an introductory course on computer engineering or in a combination of courses on digital design and software engineering.

EURO-DAC ...

EUROPEAN DESIGN AUTOMATION CONFERENCE : EURO-VHDL ...: [PROCEEDINGS].

APPLICATIONS OF POWER ELECTRONICS

VOLUME 1

[MDPI](#) Power electronics technology is still an emerging technology, and it has found its way into many applications, from renewable energy generation (i.e., wind power and solar power) to electrical vehicles (EVs), biomedical devices, and small appliances, such as laptop chargers. In the near future, electrical energy will be provided and handled by power electronics and consumed through power electronics; this not only will intensify the role of power electronics technology in power conversion processes, but also implies that power systems are undergoing a paradigm shift, from centralized distribution to distributed generation. Today, more than 1000 GW of renewable energy generation sources (photovoltaic (PV) and wind) have been installed, all of which are handled by power electronics technology. The main aim of this book is to highlight and address recent breakthroughs in the range of emerging applications in power electronics and in harmonic and electromagnetic interference (EMI) issues at device and system levels as discussed in robust and reliable power electronics technologies, including fault prognosis and diagnosis technique stability of grid-connected converters and smart control of power electronics in devices, microgrids, and at system levels.

FPGA-BASED EMBEDDED SYSTEM DEVELOPER'S GUIDE

[CRC Press](#) The book covers various aspects of VHDL programming and FPGA interfacing with examples and sample codes giving an overview of VLSI technology, digital circuits design with VHDL, programming, components, functions and procedures, and arithmetic designs followed by coverage of the core of external I/O programming, algorithmic state machine based system design, and real-world interfacing examples.

CRYPTOGRAPHIC ENGINEERING

[Springer Science & Business Media](#) This book is for engineers and researchers working in the embedded hardware industry. This book addresses the design aspects of cryptographic hardware and embedded software. The authors provide tutorial-type material for professional engineers and computer information specialists.

SPRINGER HANDBOOK OF AUTOMATION

[Springer Science & Business Media](#) This handbook incorporates new developments in automation. It also presents a widespread and well-structured conglomeration of new emerging application areas, such as medical systems and health, transportation, security and maintenance, service, construction and retail as well as production or logistics. The handbook is not only an ideal resource for automation experts but also for people new to this expanding field.

PROCEEDINGS OF INTERNATIONAL CONFERENCE ON ARTIFICIAL INTELLIGENCE, SMART GRID AND SMART CITY APPLICATIONS

AISGSC 2019

[Springer Nature](#) Due to the complexity, and heterogeneity of the smart grid and the high volume of information to be processed, artificial intelligence techniques and computational intelligence appear to be some of the enabling technologies for its future development and success. The theme of the book is "Making pathway for the grid of future" with the emphasis on trends in Smart Grid, renewable interconnection issues, planning-operation-control and reliability of grid, real time monitoring and protection, market, distributed generation and power distribution issues, power electronics applications, computer-IT and signal processing applications, power apparatus, power engineering education and industry-institute collaboration. The primary objective of the book is to review the current state of the art of the most relevant artificial intelligence techniques applied to the different issues that arise in the smart grid development.

IMPLEMENTING SOFTWARE DEFINED RADIO

Springer Science & Business Media **Software Defined Radio** makes wireless communications easier, more efficient, and more reliable. This book bridges the gap between academic research and practical implementation. When beginning a project, practicing engineers, technical managers, and graduate students can save countless hours by considering the concepts presented in these pages. The author covers the myriad options and trade-offs available when selecting an appropriate hardware architecture. As demonstrated here, the choice between hardware- and software-centric architecture can mean the difference between meeting an aggressive schedule and bogging down in endless design iterations. Because of the author's experience overseeing dozens of failed and successful developments, he is able to present many real-life examples. Some of the key concepts covered are: Choosing the right architecture for the market - laboratory, military, or commercial, Hardware platforms - FPGAs, GPPs, specialized and hybrid devices, Standardization efforts to ensure interoperability and portability State-of-the-art components for radio frequency, mixed-signal, and baseband processing. The text requires only minimal knowledge of wireless communications; whenever possible, qualitative arguments are used instead of equations. An appendix provides a quick overview of wireless communications and introduces most of the concepts the readers will need to take advantage of the material. An essential introduction to SDR, this book is sure to be an invaluable addition to any technical bookshelf.

ADVANCES IN COMPUTER, INFORMATION, AND SYSTEMS SCIENCES, AND ENGINEERING

PROCEEDINGS OF IETA 2005, TENE 2005 AND EIAE 2005

Springer Science & Business Media The conference proceedings of: International Conference on Industrial Electronics, Technology & Automation (IETA 05) International Conference on Telecommunications and Networking (TeNe 05) International Conference on Engineering Education, Instructional Technology, Assessment, and E-learning (EIAE 05) include a set of rigorously reviewed world-class manuscripts addressing and detailing state-of-the-art research projects in the areas of: Industrial Electronics, Technology and Automation, Telecommunications, Networking, Engineering Education, Instructional Technology and e-Learning. The three conferences, (IETA 05, TENE 05 and EIAE 05) were part of the International Joint Conference on Computer, Information, and System Sciences, and Engineering (CISSE 2005). CISSE 2005, the World's first Engineering/Computing and Systems Research E-Conference was the first high-caliber Research Conference in the world to be completely conducted online in real-time via the internet. CISSE received 255 research paper submissions and the final program included 140 accepted papers, from more than 45 countries. The whole concept and format of CISSE 2005 was very exciting and ground-breaking. The powerpoint presentations, final paper manuscripts and time schedule for live presentations over the web had been available for 3 weeks prior to the start of the conference for all registrants, so they could pick and choose the presentations they want to attend and think about questions that they might want to ask. The live audio presentations were also recorded and are part of the permanent CISSE archive, which includes all power point presentations, papers and recorded presentations. All aspects of the conference were managed on-line; not only the reviewing, submissions and registration processes; but also the actual conference. Conference participants - authors, presenters and attendees - only needed an internet connection and sound available on their computers in order to be able to contribute and participate in this international ground-breaking conference. The on-line structure of this high-quality event allowed academic professionals and industry participants to contribute work and attend world-class technical presentations based on rigorously refereed submissions, live, without the need for investing significant travel funds or time out of the office. Suffice to say that CISSE received submissions from more than 50 countries, for whose researchers, this opportunity presented a much more affordable, dynamic and well-planned event to attend and submit their work to, versus a classic, on-the-ground conference. The CISSE conference audio room provided superb audio even over low speed internet connections, the ability to display PowerPoint presentations, and cross-platform compatibility (the conferencing software runs on Windows, Mac, and any other operating system that supports Java). In addition, the conferencing system allowed for an unlimited number of participants, which in turn granted CISSE the opportunity to allow all participants to attend all presentations, as opposed to limiting the number of available seats for each session. The implemented conferencing technology, starting with the submission & review system and ending with the online conferencing capability, allowed CISSE to conduct a very high quality, fulfilling event for all participants. See: www.cissee2005.org, sections: IETA, TENE, EIAE

ELECTROMAGNETICS AND NETWORK THEORY AND THEIR MICROWAVE TECHNOLOGY APPLICATIONS

A TRIBUTE TO PETER RUSSE

Springer Science & Business Media This volume provides a discussion of the challenges and perspectives of electromagnetics and network theory and their microwave applications in all aspects. It collects the most interesting contribution of the symposium dedicated to Professor Peter Russer held in October 2009 in Munich.

ANALOG/RF AND MIXED-SIGNAL CIRCUIT SYSTEMATIC DESIGN

Springer Science & Business Media Despite the fact that in the digital domain, designers can take full benefits of IPs and design automation tools to synthesize and design very complex systems, the analog designers' task is still considered as a 'handcraft', cumbersome and very time consuming process. Thus, tremendous efforts are being deployed to develop new design methodologies in the analog/RF and mixed-signal domains. This book collects 16 state-of-the-art contributions devoted to the topic of systematic design of analog, RF and mixed signal circuits. Divided in the two

parts Methodologies and Techniques recent theories, synthesis techniques and design methodologies, as well as new sizing approaches in the field of robust analog and mixed signal design automation are presented for researchers and R/D engineers.

THE ZYNQ BOOK

EMBEDDED PROCESSING WITH THE ARM CORTEX-A9 ON THE XILINX ZYNQ-7000 ALL PROGRAMMABLE SOC

This book is about the Zynq-7000 All Programmable System on Chip, the family of devices from Xilinx that combines an application-grade ARM Cortex-A9 processor with traditional FPGA logic fabric. Catering for both new and experienced readers, it covers fundamental issues in an accessible way, starting with a clear overview of the device architecture, and an introduction to the design tools and processes for developing a Zynq SoC. Later chapters progress to more advanced topics such as embedded systems development, IP block design and operating systems. Maintaining a 'real-world' perspective, the book also compares Zynq with other device alternatives, and considers end-user applications. The Zynq Book is accompanied by a set of practical tutorials hosted on a companion website. These tutorials will guide the reader through first steps with Zynq, following on to a complete, audio-based embedded systems design.

INTRODUCTION TO DIGITAL DESIGN USING DIGILENT FPGA BOARDS

BLOCK DIAGRAM / VERILOG EXAMPLES

EVOLUTIONARY COMPUTING AND MOBILE SUSTAINABLE NETWORKS

PROCEEDINGS OF ICECMSN 2020

[Springer Nature](#) This book features selected research papers presented at the International Conference on Evolutionary Computing and Mobile Sustainable Networks (ICECMSN 2020), held at the Sir M. Visvesvaraya Institute of Technology on 20-21 February 2020. Discussing advances in evolutionary computing technologies, including swarm intelligence algorithms and other evolutionary algorithm paradigms which are emerging as widely accepted descriptors for mobile sustainable networks virtualization, optimization and automation, this book is a valuable resource for researchers in the field of evolutionary computing and mobile sustainable networks.

ICICCT 2019 - SYSTEM RELIABILITY, QUALITY CONTROL, SAFETY, MAINTENANCE AND MANAGEMENT

APPLICATIONS TO ELECTRICAL, ELECTRONICS AND COMPUTER SCIENCE AND ENGINEERING

[Springer](#) This book discusses reliability applications for power systems, renewable energy and smart grids and highlights trends in reliable communication, fault-tolerant systems, VLSI system design and embedded systems. Further, it includes chapters on software reliability and other computer engineering and software management-related disciplines, and also examines areas such as big data analytics and ubiquitous computing. Outlining novel, innovative concepts in applied areas of reliability in electrical, electronics and computer engineering disciplines, it is a valuable resource for researchers and practitioners of reliability theory in circuit-based engineering domains.

PRINCIPLES OF COMMUNICATION SYSTEMS SIMULATION WITH WIRELESS APPLICATIONS

[Prentice Hall](#) This volume presents an overview of computer-based simulation models and methodologies for communication systems. Topics covered include probability, random, process, and estimation theory and roles in the design of computer-based simulations.

FPGA PROTOTYPING BY VHDL EXAMPLES

XILINX MICROBLAZE MCS SOC

[John Wiley & Sons](#) A hands-on introduction to FPGA prototyping and SoC design This Second Edition of the popular book follows the same "learning-by-doing" approach to teach the fundamentals and practices of VHDL synthesis and FPGA prototyping. It uses a coherent series of examples to demonstrate the process to develop sophisticated digital circuits and IP (intellectual property) cores, integrate them into an SoC (system on a chip) framework, realize the system on an FPGA prototyping board, and verify the hardware and software operation. The examples start with simple gate-level circuits, progress gradually through the RT (register transfer) level modules, and lead to a functional embedded system with custom I/O peripherals and hardware accelerators. Although it is an introductory text, the examples are developed in a rigorous manner, and the derivations follow strict design guidelines and coding practices used for large, complex digital systems. The new edition is completely updated. It presents the hardware design in the SoC context and introduces the hardware-software co-design concept. Instead of treating examples as isolated entities, the book integrates them into a single coherent SoC platform that allows readers to explore both hardware and software "programmability" and develop complex and interesting embedded system projects. The revised edition: Adds four general-purpose IP cores, which are multi-channel PWM (pulse width modulation) controller, I2C controller, SPI controller, and XADC (Xilinx analog-to-digital converter) controller. Introduces a music synthesizer constructed with a DDS (direct digital frequency synthesis) module and an ADSR (attack-decay-sustain-release) envelop generator. Expands the original video controller into a complete stream-based video subsystem that incorporates a video synchronization circuit, a test pattern generator, an OSD (on-screen display) controller, a sprite generator, and a frame

buffer. Introduces basic concepts of software-hardware co-design with Xilinx MicroBlaze MCS soft-core processor. Provides an overview of bus interconnect and interface circuit. Introduces basic embedded system software development. Suggests additional modules and peripherals for interesting and challenging projects. The FPGA Prototyping by VHDL Examples, Second Edition makes a natural companion text for introductory and advanced digital design courses and embedded system course. It also serves as an ideal self-teaching guide for practicing engineers who wish to learn more about this emerging area of interest.

ADVANCED FORMAL VERIFICATION

Springer Science & Business Media **Advanced Formal Verification** shows the latest developments in the verification domain from the perspectives of the user and the developer. World leading experts describe the underlying methods of today's verification tools and describe various scenarios from industrial practice. In the first part of the book the core techniques of today's formal verification tools, such as SAT and BDDs are addressed. In addition, multipliers, which are known to be difficult, are studied. The second part gives insight in professional tools and the underlying methodology, such as property checking and assertion based verification. Finally, analog components have to be considered to cope with complete system on chip designs.

EMBEDDED SYSTEMS: AN INTEGRATED APPROACH

Pearson Education India **Embedded Systems: An Integrated Approach** is exclusively designed for the undergraduate courses in electronics and communication engineering as well as computer science engineering. This book is well-structured and covers all the important processors and their applications in a sequential manner. It begins with a highlight on the building blocks of the embedded systems, moves on to discuss the software aspects and new processors and finally concludes with an insightful study of important applications. This book also contains an entire part dedicated to the ARM processor, its software requirements and the programming languages. Relevant case studies and examples supplement the main discussions in the text.

PROCEEDING OF THE SECOND INTERNATIONAL CONFERENCE ON MICROELECTRONICS, COMPUTING & COMMUNICATION SYSTEMS (MCCS 2017)

Springer The volume presents high quality papers presented at the Second International Conference on Microelectronics, Computing & Communication Systems (MCCS 2017). The book discusses recent trends in technology and advancement in MEMS and nanoelectronics, wireless communications, optical communication, instrumentation, signal processing, image processing, bioengineering, green energy, hybrid vehicles, environmental science, weather forecasting, cloud computing, renewable energy, RFID, CMOS sensors, actuators, transducers, telemetry systems, embedded systems, and sensor network applications. It includes original papers based on original theoretical, practical, experimental, simulations, development, application, measurement, and testing. The applications and solutions discussed in the book will serve as a good reference material for future works.

ADVANCES IN IMAGE AND DATA PROCESSING USING VLSI DESIGN

SMART VISION SYSTEMS. VOLUME 1

VLSI is a well-established field of research that ignited the modern computing revolution. Serving as a guide to future developments, this book provides a framework for design, modeling concepts, and application of Image Processing based systems using VLSI design techniques.

VLSI DESIGN AND TEST

17TH INTERNATIONAL SYMPOSIUM, VDAT 2013, JAIPUR, INDIA, JULY 27-30, 2013, PROCEEDINGS

Springer This book constitutes the refereed proceedings of the 17th International Symposium on VLSI Design and Test, VDAT 2013, held in Jaipur, India, in July 2013. The 44 papers presented were carefully reviewed and selected from 162 submissions. The papers discuss the frontiers of design and test of VLSI components, circuits and systems. They are organized in topical sections on VLSI design, testing and verification, embedded systems, emerging technology.

COMMUNICATION SYSTEMS

AN INTRODUCTION TO SIGNALS AND NOISE IN ELECTRICAL COMMUNICATION

ANALOG CIRCUIT DESIGN

SENSORS, ACTUATORS AND POWER DRIVERS; INTEGRATED POWER AMPLIFIERS FROM WIRELINE TO RF; VERY HIGH FREQUENCY FRONT ENDS

Springer Science & Business Media **Analog Circuit Design** is based on the yearly **Advances in Analog Circuit Design** workshop. The aim of the workshop is to bring together designers of advanced analogue and RF circuits for the purpose of studying and discussing new possibilities and future developments in this field. Selected topics for AACD 2007 were: (1) Sensors, Actuators and Power Drivers for the Automotive and Industrial Environment; (2) Integrated PA's from Wireline to RF; (3) Very High Frequency Front Ends.

SATELLITE COMMUNICATIONS AND NAVIGATION SYSTEMS

[Springer](#) **Satellite Communications and Navigation Systems** publishes the proceedings of the 2006 Tyrrhenian International Workshop on Digital Communications. The book focuses on the integration of communication and navigation systems in satellites.

DIGITAL DESIGN

A SYSTEMS APPROACH

[Cambridge University Press](#) **Provides students with a system-level perspective and the tools they need to understand, analyze and design complete digital systems using Verilog. It goes beyond the design of simple combinational and sequential modules to show how such modules are used to build complete systems, reflecting digital design in the real world.**

DIGITAL DESIGN

USING DIGILENT FPGA BOARDS ; VHDL, ACTIVE-HDL EDITION ; [75+ VHDL EXAMPLES]

INTELLIGENT COMMUNICATION, CONTROL AND DEVICES

PROCEEDINGS OF ICICCD 2017

[Springer](#) **The book focuses on the integration of intelligent communication systems, control systems, and devices related to all aspects of engineering and sciences. It contains high-quality research papers presented at the 2nd international conference, ICICCD 2017, organized by the Department of Electronics, Instrumentation and Control Engineering of University of Petroleum and Energy Studies, Dehradun on 15 and 16 April, 2017. The volume broadly covers recent advances of intelligent communication, intelligent control and intelligent devices. The work presented in this book is original research work, findings and practical development experiences of researchers, academicians, scientists and industrial practitioners.**

DISCRETE-EVENT MODELING AND SIMULATION

THEORY AND APPLICATIONS

[CRC Press](#) **Collecting the work of the foremost scientists in the field, Discrete-Event Modeling and Simulation: Theory and Applications presents the state of the art in modeling discrete-event systems using the discrete-event system specification (DEVS) approach. It introduces the latest advances, recent extensions of formal techniques, and real-world examples of various applications. The book covers many topics that pertain to several layers of the modeling and simulation architecture. It discusses DEVS model development support and the interaction of DEVS with other methodologies. It describes different forms of simulation supported by DEVS, the use of real-time DEVS simulation, the relationship between DEVS and graph transformation, the influence of DEVS variants on simulation performance, and interoperability and composability with emphasis on DEVS standardization. The text also examines extensions to DEVS, new formalisms, and abstractions of DEVS models as well as the theory and analysis behind real-world system identification and control. To support the generation and search of optimal models of a system, a framework is developed based on the system entity structure and its transformation to DEVS simulation models. In addition, the book explores numerous interesting examples that illustrate the use of DEVS to build successful applications, including optical network-on-chip, construction/building design, process control, workflow systems, and environmental models. A one-stop resource on advances in DEVS theory, applications, and methodology, this volume offers a sampling of the best research in the area, a broad picture of the DEVS landscape, and trend-setting applications enabled by the DEVS approach. It provides the basis for future research discoveries and encourages the development of new applications.**

NANOELECTRONIC MIXED-SIGNAL SYSTEM DESIGN

[McGraw Hill Professional](#) **Covering both the classical and emerging nanoelectronic technologies being used in mixed-signal design, this book addresses digital, analog, and memory components. Winner of the Association of American Publishers' 2016 PROSE Award in the Textbook/Physical Sciences & Mathematics category. Nanoelectronic Mixed-Signal System Design offers professionals and students a unified perspective on the science, engineering, and technology behind nanoelectronics system design. Written by the director of the NanoSystem Design Laboratory at the University of North Texas, this comprehensive guide provides a large-scale picture of the design and manufacturing aspects of nanoelectronic-based systems. It features dual coverage of mixed-signal circuit and system design, rather than just digital or analog-only. Key topics such as process variations, power dissipation, and security aspects of electronic system design are discussed. Top-down analysis of all stages--from design to manufacturing Coverage of current and developing nanoelectronic technologies--not just nano-CMOS Describes the basics of nanoelectronic technology and the structure of popular electronic systems Reveals the techniques required for design excellence and manufacturability**